

AMENDMENT UNDER 37 C.F.R. § 1.111
U. S. Application No. 09/472,869

a format converter for receiving either of a first input digital signal and a second input digital signal, according to which of the first and second input digital signals is present, the format converter for converting the input digital signal received by the format converter into a predetermined display format output signal; and

a controller for controlling the switching portion to select and output the clock frequency corresponding to a frame rate of the input digital signal.

10. (Amended) A method of adapting clock frequency in digital signal receiver to correspond with a frame rate of an input broadcast signal, said method comprising:
receiving said input broadcast signal into said digital signal receiver;
detecting a frame rate of the input broadcast signal received;
selecting a clock frequency that corresponds to the frame rate which is detected; and
outputting the clock frequency which is selected to components of the digital signal receiver that use the clock frequency to convert the input broadcast signal into a predetermined display format output signal.

11. (Amended) The method according to claim 10 wherein the step of selecting the clock frequency comprises, outputting a control signal from a controller, said control signal depending upon the frame rate which is detected; receiving said control signal into a selector, said selector connected to outputs of a plurality of phase locked loops, wherein each phase locked loop has a predetermined clock frequency, and selecting one predetermined clock frequency of one of said plurality of phase locked loops based upon the control signal received by the selector.